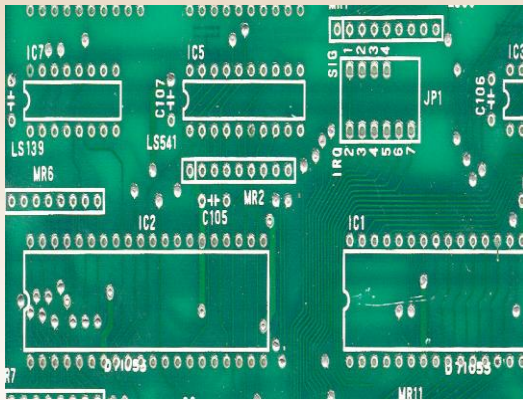


Registration:

- Prior registration for the workshop is mandatory.
- Last date for registration: 5th of February 2016
- Refreshment will be provided.

Workshop conducted by:

- Industrial Expert
- Departmental Faculty



Chief Patron:

Shri V.M. Patel, President, KSV

Patron:

Prof. D. T. Kapadia, Director, KSV

Prof.A.J Patel, Principal,

VPMP Polytechnic

Advisory Committee:

Mr. N.B PATEL

Mr. C.H .Bhatt

I/C Head, EC Dept

I/C Head, EC Dept.

Co-ordinator

Mr.Dharmesh B. Patel

Co- coordinator:

Mrs. Seema Patel

Mr. Ashish Patel

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Mr. Ashish Patel

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ONE DAY WORKSHOP

On

PCB Design & Simulation

Jointly Organized By

ISTE STUDENT CHAPTER

&

ELECTRONICS & COMMUNICATION
DEPARTMENT



VPMP POLYTECHNIC

Sector 15, Near Kh-5,
LDRP-ITR Campus
Gandhinagar Gujarat- 382015

About Workshop

The workshop will focus on PCB designing
Followed by essentials of schematic and
Layout design using ORCAD.

Date of Workshop: 8th of February 2016

Time: 9:00 AM to 5:00PM

Venue: VPMP POLYTECHNIC, Gandhinagar

Who should attend this workshop?

- Engineering students.
- A who find PCB design capability as job.
- A electronics hobbyist

Work shop Content

- Printed Circuit board Concept
- PCB Material
- PCB manufacture process flow
- PCB chemicals
- Capture basics
- Creating design
- Processing design
- Circuit Simulation
- Creating PCB using layout

About Electronics & communication Department

- Electronics and communication Department of VPMP POLYTECHNIC is potential and versatile.
- We are having 120 seats (60 (F.S.) + 60 (S.S.)) for the students of this branch.
- Department staff is of high caliber and having wide Experience in research and development and work Experience in public sector.
- The courses run for the different semester covers all the modern technologies that are required for good Electronic and Communication Engineer.
- The department is also having modern facility conducting practical and also having modern application software which are required for the development of practical skill for the students.

Programme Objectives

This Work shop will provide Knowledge about PCB Design & Simulation techniques and modern manufacturing Process.

Learning PCB design considerations and rules.
Developing skill in PCB design, and Routing.

About Institute

- The VPMP Polytechnic Gandhinagar is one of the best diploma institute in the capital of Gujarat.
- The college was established in 2006 for making perfect junior engineer, planner and statesman of the country.
- VPMP has a beautiful green campus in the heart of the capital of about 10 acres near Vidhan sabha Bhawan .
- All departments & laboratories are housed on the campus.
- Hostels accommodating about 400 boys and 250 Girls students.

Resource Persons

Workshop will be conducted by EC Department VPMP Polytechnic.

Workshop Fee

Workshop fee

Rs. 200