

# VPMP POLYTECHNIC, GANDHINAGAR

## Schedule of One Day Workshop on PCB Designing and Simulation

**Workshop Date: 08/02/2016**

SESSION	TIME	SESSIONS CONDUCTED BY
Registration / tea	9:00 a.m to 9:30 a.m	-
PCB theory	9:30 a.m to 11:30 a.m	Mr.JAYANTIBHAI PATEL (Dutt Electronics,Gandhinagar)
Lunch break	11:30 a.m to 12:30 p.m	-
Schematic and simulation	12:30 p.m to 2:30 p.m	Mr.D.B PATEL Mrs. S. A. PATEL
Tea break	2:30 p.m to 2:45 p.m	
PCB layout	2:4 5p.m to 5:00 p.m	Mr. D.B. PATEL Mrs. S.A. PATEL